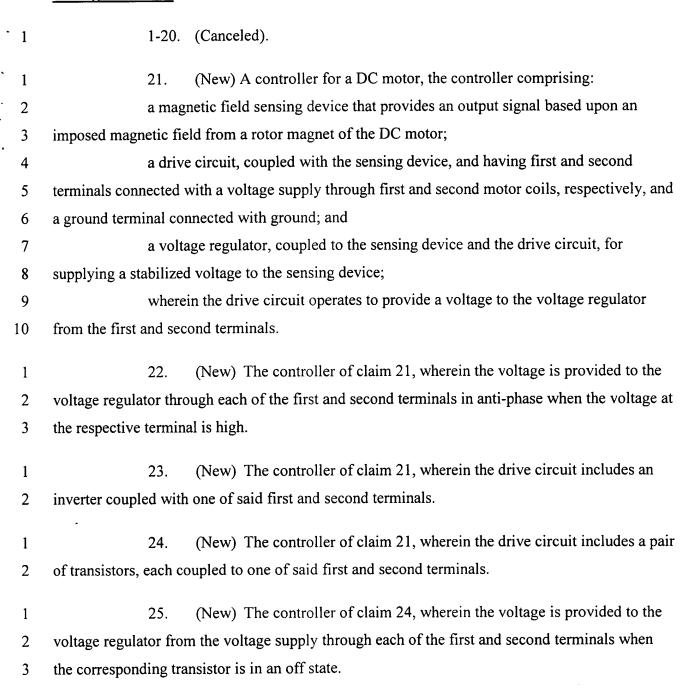
Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:



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1	26. (New) The controller of claim 25, wherein the transistor is set to an off
2	state based on a state of the output signal.
1	27. (New) The controller of claim 21, wherein the voltage regulator supplies a
2	stabilized voltage to the drive circuit and wherein the drive circuit operates to drive the DC
3	motor.
1	28. (New) The controller of claim 21, wherein the drive circuit further
2	includes a switching circuit configured to electrically couple the voltage regulator with each
3	voltage terminal in anti-phase when the voltage at the respective voltage terminal is high.
1	29. (New) A controller for a DC motor, the controller comprising:
2	a magnetic field sensing device that provides an output signal based upon an
3	imposed magnetic field from a rotor magnet of the DC motor;
4	a drive circuit, coupled with the sensing device, and having a ground terminal
5	connected with ground, a first transistor coupled to a first terminal, and a second transistor
6	coupled to a second terminal, wherein each of said first and second terminals is coupled to a
7	voltage supply through a separate motor coil; and
8	a voltage regulator coupled to the sensing device and the drive circuit, for
9	supplying a stabilized voltage to the sensing device;
10	wherein the drive circuit operates to provide a voltage to the voltage regulator
11	from the first and second terminals.
1	30. (New) The controller of claim 29, wherein the voltage is alternately
2	provided to the voltage regulator through each of the first and second terminals when the
3	corresponding transistor is in an off state.
1	31. (New) The controller of claim 30, wherein each transistor is set to an off
2	state based on a state of the output signal.

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- 1 32. (New) The controller of claim 30, wherein the drive circuit further 2 includes a switching circuit configured to alternately couple the voltage regulator with each of 3 the first and second terminals when the corresponding transistor is in the off state.
- 1 33. (New) The controller of claim 29, wherein the controller is a package 2 having only three external connection terminals.
- 1 34. (New) The controller of claim 29, wherein the drive circuit includes an inverter coupled with one of said transistors.